

	L #	Hit	S arch T xt	DBs
1	L1	2	("5779839" "5528222").pn.	USP AT; US-P GPU B
2	L2	1870	(361/749,750,751,767,7 68,769,770,771).cccls.	USP AT; US-P GPU B
3	L3	1969	(174/254,256,258,259). cccls.	USP AT; US-P GPU B
4	L4	337	257/679.cccls.	USP AT; US-P GPU B
5	L5	2282	(235/488,489,491,492). cccls.	USP AT; US-P GPU B
6	L6	1204	428/211.cccls.	USP AT; US-P GPU B
7	L7	1457	(340/572.1,572.5,572.7, 572.8).cccls.	USP AT; US-P GPU B

	L #	Hit	arch T xt	DBs
8	L8	887	(438/107,110).ccls.	USP AT; US-P GPU B
9	L9	918	29/830.ccls.	USP AT; US-P GPU B
10	L10	1024 5	2 3 4 5 6 7 8 9	USP AT; US-P GPU B
11	L11	476	10 and @pd>=20030301	USP AT; US-P GPU B
12	L13	155	11 and (flex flexible)	USP AT; US-P GPU B
13	L12	100	11 and (crease\$1 fold\$4 bend\$4)	USP AT; US-P GPU B
14	L14	95	13 not 12	USP AT; US-P GPU B

	L #	Hits	S arch Text	DBs
15	L15	17	14 and paper	USP AT; US-P GPU B
16	L16	78	14 not 15	USP AT; US-P GPU B
17	L17	281	11 not (12 14)	USP AT; US-P GPU B
18	L18	40	17 and paper	USP AT; US-P GPU B
19	L19	241	17 not 18	USP AT; US-P GPU B
20	L20	99	19 and (antenna\$1 capacitor\$1 condenser\$1)	USP AT; US-P GPU B